SIMULTANEOUS BIDIRECTIONAL INPUT/OUTPUT CIRCUIT AND METHOD ABSTRACT OF THE DISCLOSURE

Method and apparatus for use with simultaneous bi-directional (SBD) input/output circuits are included among the embodiments. In exemplary systems, the receiver in an SBD circuit compares a bit line voltage to two different voltages representing the two voltages that are expected on the bit line, based on the data that the driver in the SBD circuit is currently driving. Other embodiments are described and claimed.

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